



We cordially invite you to the

***Conference on
Electronic Systems &
Expo 2018***

on Tuesday 26th June 2018

9.30 am - 5.30 pm

at DESE, IISc, Bengaluru



Department of Electronic Systems Engineering
Indian Institute of Science, Bengaluru 560 012

Time	Title	Venue
9:30 to 9:45	Inauguration and Welcome Address	DESE Auditorium
9:45 to 10:00	Coffee/Tea	
10:00 to 17:00	Session: FPGA and ASIC Design	
	A high throughput kernel co-processor for SVM	Communication Networks Lab
	Runtime configurable co-processor for binary weighted CNN	
	Brain/Synapse chip	Nano Devices Research Lab-317
	Solving differential equations using FPGA	Nano Scale Device Research Lab
	Multicore RISC V ASIC	Communication Networks Lab
	Radar-on-chip design for strategic applications	ARSL Lab, ECE
	Session: Communication and Healthcare Systems	
	IOT enabled healthcare devices	Embedded Sys Lab
	Enterprise Network Security: A Behavioural Approach	Network Lab 3
	Securing web services	Embedded Sys Lab
	RF Sensor Platform	TI-DSP Lab
	Smart walking stick and activity detector	Embedded Sys Lab
	Long haul Drones	DESE Terrace
17:00 to 17:15	Concluding Remarks	DESE Auditorium



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